General Description

The GreenMOS® high voltage MOSFET utilizes charge balance technology to achieve outstanding low on-resistance and lower gate charge. It is engineered to minimize conduction loss, provide superior switching performance and robust avalanche capability.

The GreenMOS® Generic series is optimized for extreme switching performance to minimize switching loss. It is tailored for high power density applications to meet the highest efficiency standards.

Features

- Low $R_{DS(ON)}$ & FOM
- Extremely low switching loss
- Excellent stability and uniformity

Applications

- PC power
- LED lighting
- Telecom power
- Server power
- EV Charger
- Solar/UPS

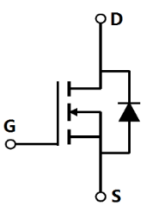
Key Performance Parameters

Parameter	Value	Unit
$V_{DS,\ min}$ @ $T_{j(max)}$	850	V
$I_{D,\ pulse}$	12	A
$R_{DS(ON)\ ,\ max}$ @ V_{GS}=10V	1.4	Ω
Q_g	7.5	nC

Marking Information

Product Name	Package	Marking
OSG80R1K4FF	TO220F	OSG80R1K4F

Package & Pin Information



Absolute Maximum Ratings at T_j=25°C unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	800	V
Gate-source voltage	V_{GS}	±30	V
Continuous drain current[1], T_C=25 °C	I_D	4	A
Continuous drain current[1], T_C=100 °C		2.5	
Pulsed drain current[2], T_C=25 °C	$I_{D,\ pulse}$	12	A
Continuous diode forward current[1], T_C=25 °C	I_S	4	A
Diode pulsed current[2], T_C=25 °C	$I_{S,\ pulse}$	12	A
Power dissipation[3], T_C=25 °C	P_D	26	W
Single pulsed avalanche energy[5]	E_{AS}	100	mJ
MOSFET dv/dt ruggedness, V_{DS}=0…640 V	dv/dt	50	V/ns
Reverse diode dv/dt, V_{DS}=0…640 V, I_{SD}≤I_D	dv/dt	15	V/ns
Operation and storage temperature	T_{stg}, T_j	-55 to 150	°C

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal resistance, junction-case	$R_{\theta JC}$	4.8	°C/W
Thermal resistance, junction-ambient[4]	$R_{\theta JA}$	62.5	°C/W

Electrical Characteristics at T_j=25°C unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Drain-source breakdown voltage	BV_{DSS}	800			V	V_{GS}=0 V, I_D=250 μA
		850	930			V_{GS}=0 V, I_D=250 μA, T_j=150 °C
Gate threshold voltage	$V_{GS(th)}$	2.0		4.0	V	V_{DS}=V_{GS}, I_D=250 μA
Drain-source on-state resistance	$R_{DS(ON)}$		1.1	1.4	Ω	V_{GS}=10 V, I_D=2 A
			2.75			V_{GS}=10 V, I_D=2 A, T_j=150 °C
Gate-source leakage current	I_{GSS}			100	nA	V_{GS}=30 V
				-100		V_{GS}=-30 V
Drain-source leakage current	I_{DSS}			1	μA	V_{DS}=800 V, V_{GS}=0 V
Gate resistance	R_G			4.1	Ω	F=1 MHz, Open drain

Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C_{iss}		363.5		pF	V_{GS}=0 V, V_{DS}=50 V, f=1 MHz
Output capacitance	C_{oss}		25.5		pF	
Reverse transfer capacitance	C_{rss}		1.35		pF	
Turn-on delay time	$t_{d(on)}$		16.8		ns	V_{GS}=10 V, V_{DS}=400 V, R_G=25 Ω, I_D=2 A
Rise time	t_r		5.7		ns	
Turn-off delay time	$t_{d(off)}$		31		ns	
Fall time	t_f		42.8		ns	

Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total gate charge	Q_g		7.5		nC	V_{GS}=10 V, V_{DS}=400 V, I_D=4 A
Gate-source charge	Q_{gs}		2.1		nC	
Gate-drain charge	Q_{gd}		2.9		nC	
Gate plateau voltage	$V_{plateau}$		5.6		V	

Body Diode Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Diode forward voltage	V_{SD}			1.3	V	I_S=4 A, V_{GS}=0 V
Reverse recovery time	t_{rr}		214.4		ns	I_S=4 A, di/dt=100 A/μs
Reverse recovery charge	Q_{rr}		1.6		μC	
Peak reverse recovery current	I_{rrm}		13.3		A	

Note

1) Calculated continuous current based on maximum allowable junction temperature.
2) Repetitive rating; pulse width limited by max. junction temperature.
3) Pd is based on max. junction temperature, using junction-case thermal resistance.
4) The value of $R_{\theta JA}$ is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with T_a=25 °C.
5) V_{DD}=50 V, V_{GS}=10 V, L=10 mH, starting T_j=25 °C.

Electrical Characteristics Diagrams

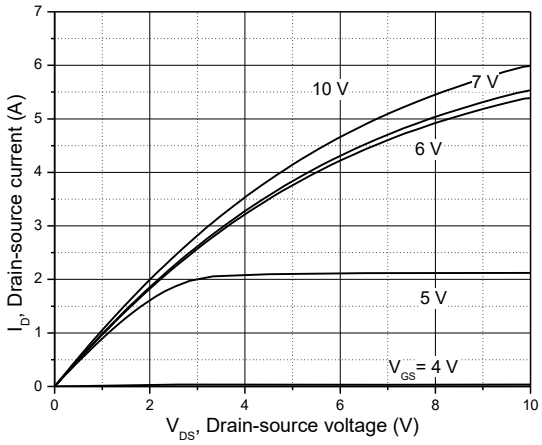


Figure 1. Typ. output characteristics

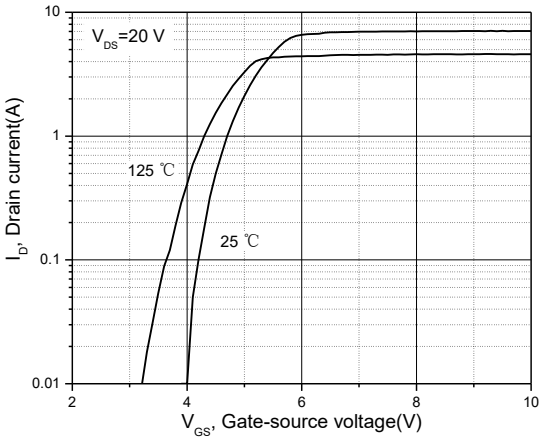


Figure 2. Typ. transfer characteristics

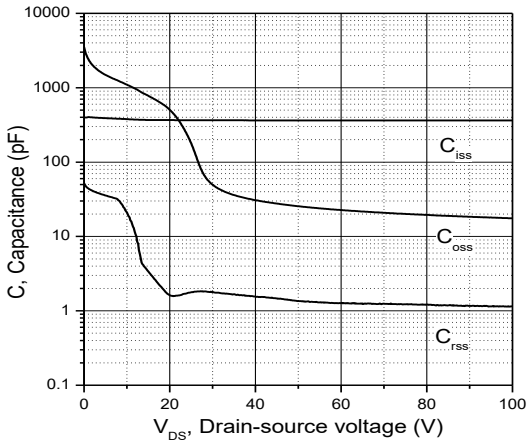


Figure 3. Typ. capacitances

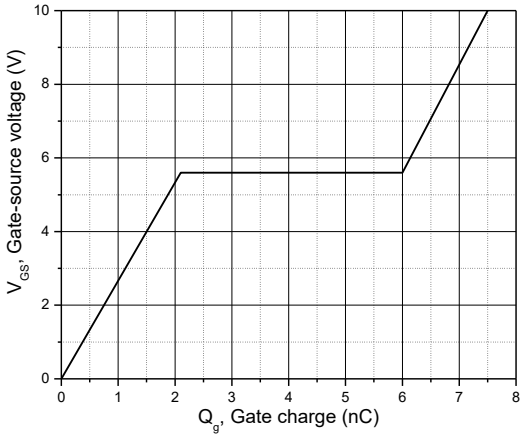


Figure 4. Typ. gate charge

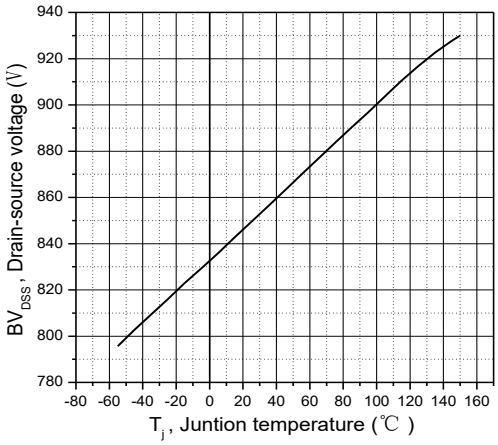


Figure 5. Drain-source breakdown voltage

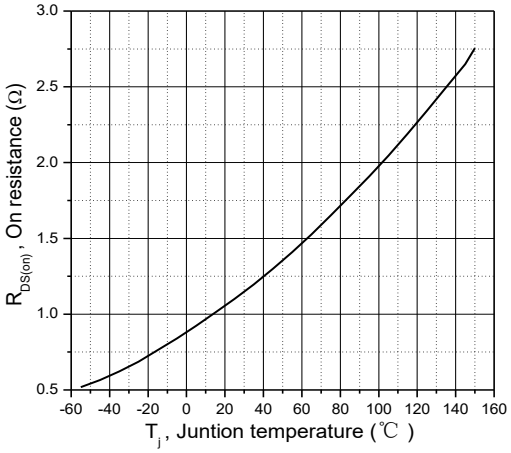


Figure 6. Drain-source on-state resistance

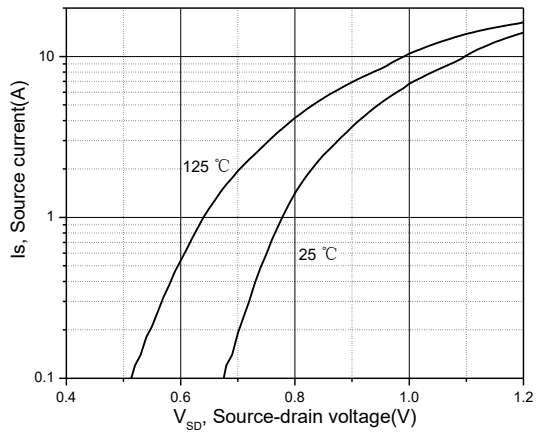


Figure 7. Forward characteristic of body diode

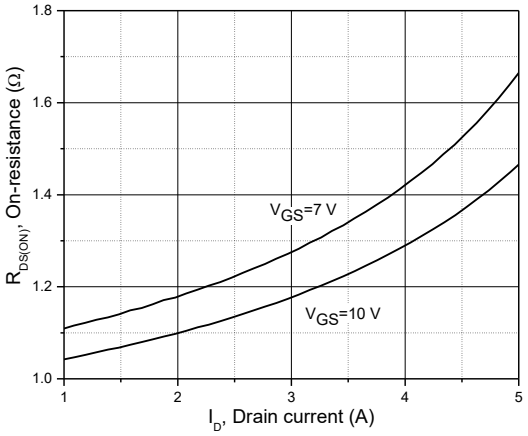


Figure 8. Drain-source on-state resistance

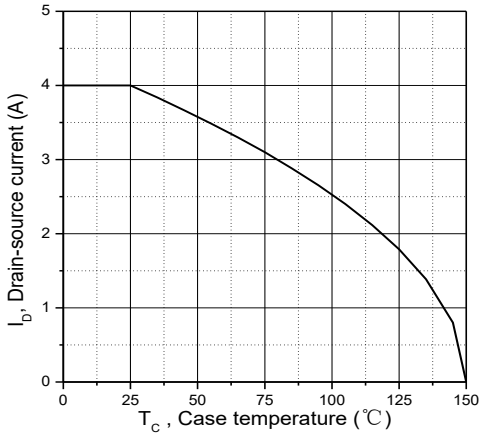


Figure 9. Drain current

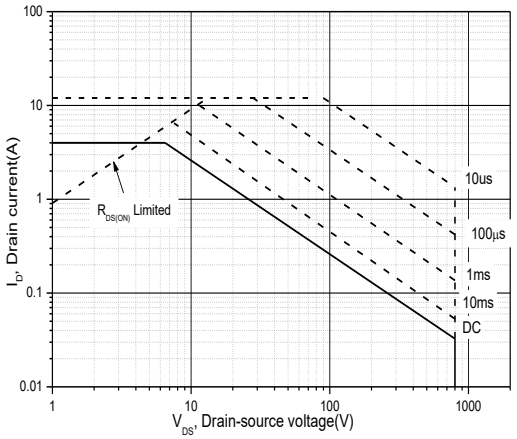


Figure 10. Safe operation area T_C=25 °C

Test circuits and waveforms

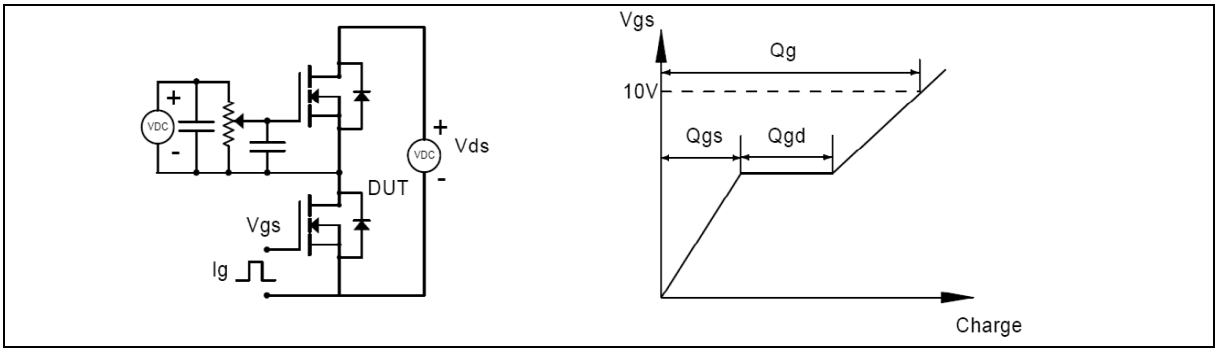


Figure 1. Gate charge test circuit & waveform

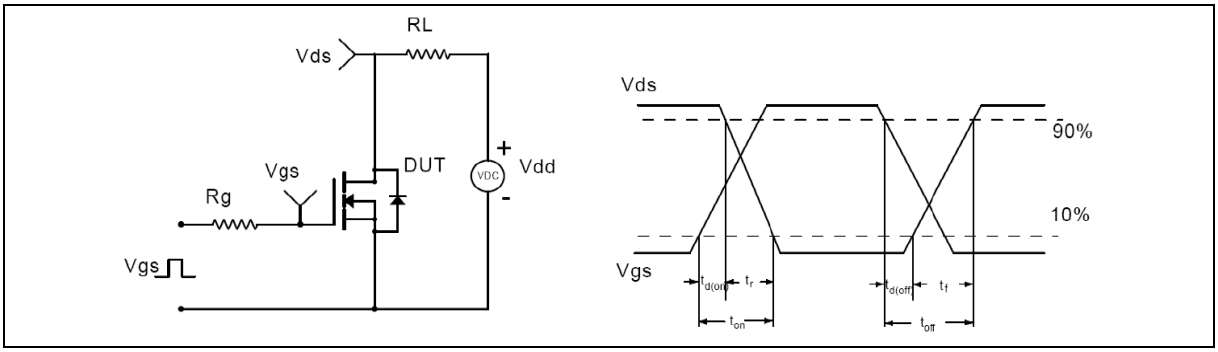


Figure 2. Switching time test circuit & waveforms

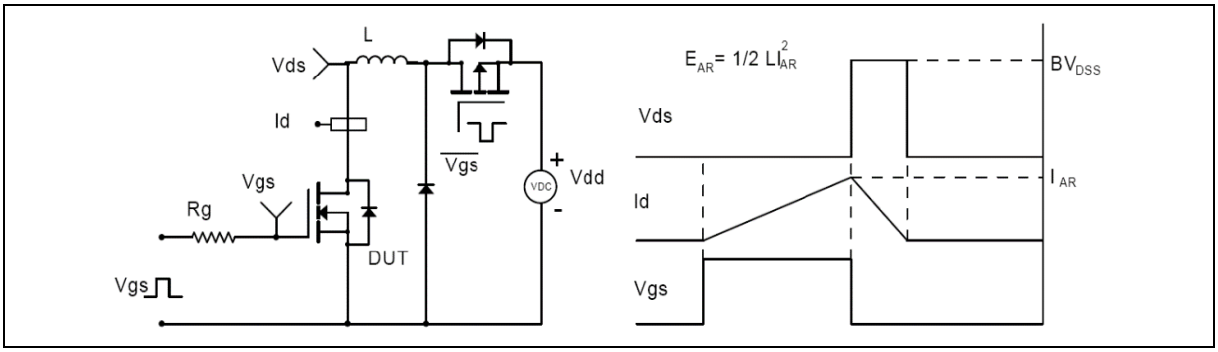


Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms

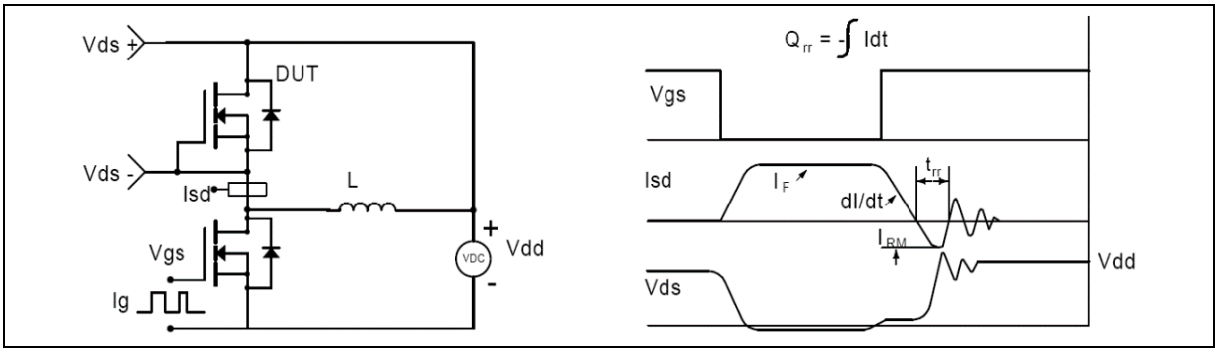
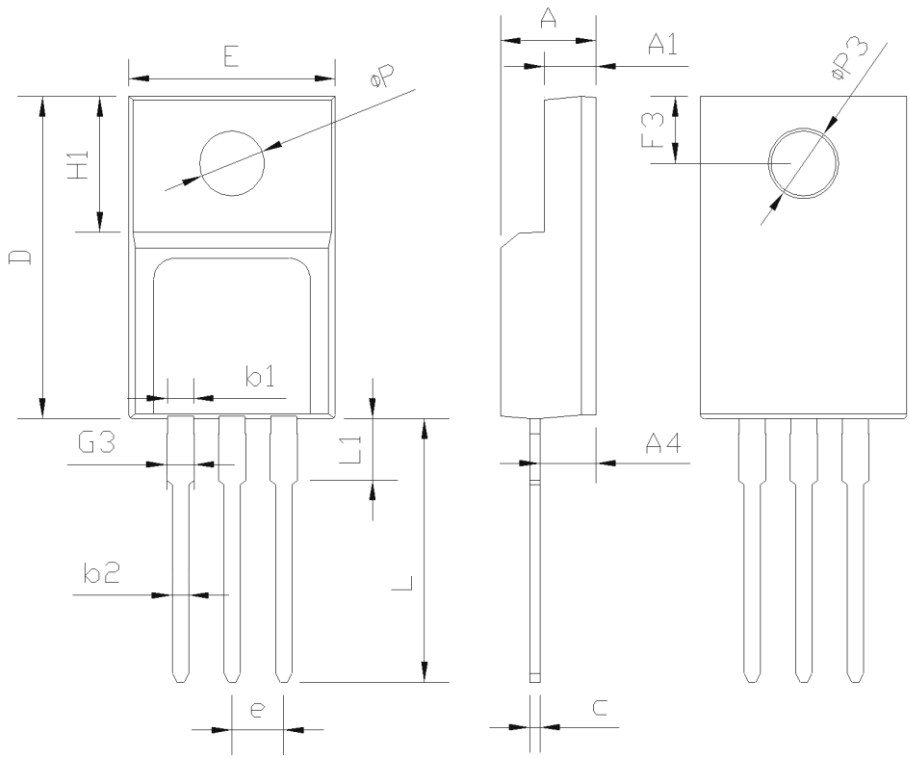


Figure 4. Diode reverse recovery test circuit & waveforms

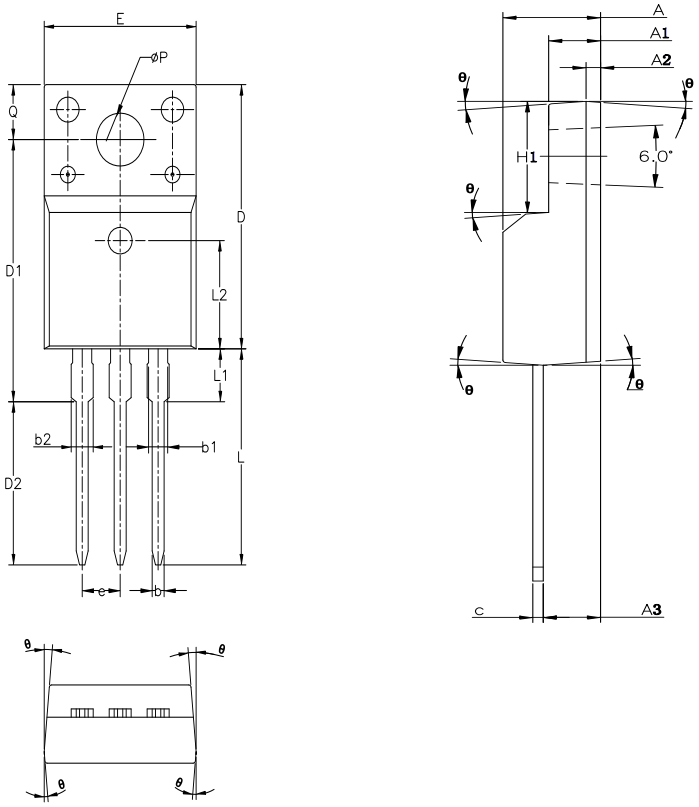
Package Information



Symbol	mm		
	Min	Nom	Max
E	9.96	10.16	10.36
A	4.50	4.70	4.90
A1	2.34	2.54	2.74
A4	2.56	2.76	2.96
c	0.40	0.50	0.65
D	15.57	15.87	16.17
H1	6.70REF		
e	2.54BSC		
L	12.68	12.98	13.28
L1	2.88	3.03	3.18
ΦP	3.03	3.18	3.38
ΦP3	3.15	3.45	3.65
F3	3.15	3.30	3.45
G3	1.25	1.35	1.55
b1	1.18	1.28	1.43
b2	0.70	0.80	0.95

Version 1: TO220F-C package outline dimension

Package Information



Symbol	mm		
	Min	Nom	Max
A	4.40	4.50	4.60
A1	1.27	1.30	1.33
A2	2.30	2.40	2.50
b	0.70	-	0.90
b1	1.27	-	1.40
c	0.45	0.50	0.60
D	15.30	15.70	16.10
D1	9.10	9.20	9.30
D2	13.10	-	13.70
E	9.70	9.90	10.20
E1	7.80	8.00	8.20
e	2.54BSC		
e1	5.08BSC		
H1	6.30	6.50	6.70
L	12.78	13.08	13.38
L1	-	-	3.50
L2	4.60REF		
ΦP	3.55	3.60	3.65
Q	2.73	-	2.87
θ1	1°	3°	5°

Version 2: TO220F-J package outline dimension

Ordering Information

Package Type	Units/ Tube	Tubes/ Inner Box	Units/ Inner Box	Inner Boxes/ Carton Box	Units/ Carton Box
TO220F-C	50	20	1000	6	6000
TO220F-J	50	20	1000	5	5000

Product Information

Product	Package	Pb Free	RoHS	Halogen Free
OSG80R1K4FF	TO220F	yes	yes	yes

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